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2-12-01INFORMATION DISCLOSURE CITATION
IN AN APPLICATION

Docket Number	Application Number
M4065.0340/P340	09/652,003 O.I.P.E.
Applicant(s)	
Graham Kirsch	
Filing Date	Group Art Unit
August 31, 2000	2185

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
wlc	A	5,581,773	12/1996	Glover			
	B	5,638,533	06/1997	Law			
	C	5,727,229	03/1998	Kan et al.			
	D	5,963,746	10/1999	Barker et al.			
	E	6,061,367	05/2000	Siemers			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translations	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

wlc	F	Michael J. Flynn, "VERY HIGH-SPEED COMPUTING SYSTEMS," Proceedings of the IEEE, Vol. 54, No. 12, December 1966, pages 1901-1909.
	G	Norman Margolus, "An Embedded DRAM Architecture for Large-Scale Spatial-Lattice Computations," Computer Architecture News Conference, Vol. 28, No. 2, May 2000; pp. 149-60.
	H	FUZION 150 Product Overview, PixelFusion Ltd, PFL/004/99 v2. Revised Jan. 2000.
	I	Gamma II Plus Technical Overview (visited July, 2000) < http://www.cppus.com/techoverview.pdf >

EXAMINER	DATE CONSIDERED
Not Yet Assigned	<i>Patricia L. C. 8/5/02</i>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.	

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